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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Mutsumi KIMURA

Application No.: 10/765,200

Docket No.: 118319

Filed: January 28, 2004

For: METHOD OF MANUFACTURING THIN FILM ELEMENT, THIN FILM
TRANSISTOR CIRCUIT SUBSTRATE, ACTIVE MATRIX DISPLAY DEVICE,
ELECTRO-OPTICAL DEVICE, AND ELECTRONIC APPARATUS

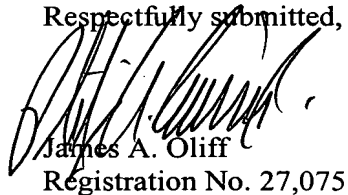
REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,



James A. Oliff
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JAO:PAC/nxy

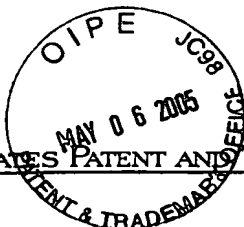
Date: May 6, 2005

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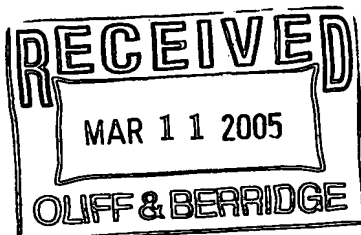


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/765,200	01/28/2004	2811	900	118319	8	6	1

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CONFIRMATION NO. 1136

UPDATED FILING RECEIPT



OC000000015423715

Date Mailed: 03/10/2005

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Applicant(s)

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Assignment For Published Patent Application

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Domestic Priority data as claimed by applicant.

MARIO A. COSTANTINO (33,565)

Foreign Applications

JAPAN 2003-19126(P) 01/28/2003

If Required, Foreign Filing License Granted: 06/14/2004

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/765,200**

Projected Publication Date: 06/16/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing thin film element, thin film transistor circuit substrate, active matrix display device, electro-optical device, and electronic apparatus

Preliminary Class

257

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